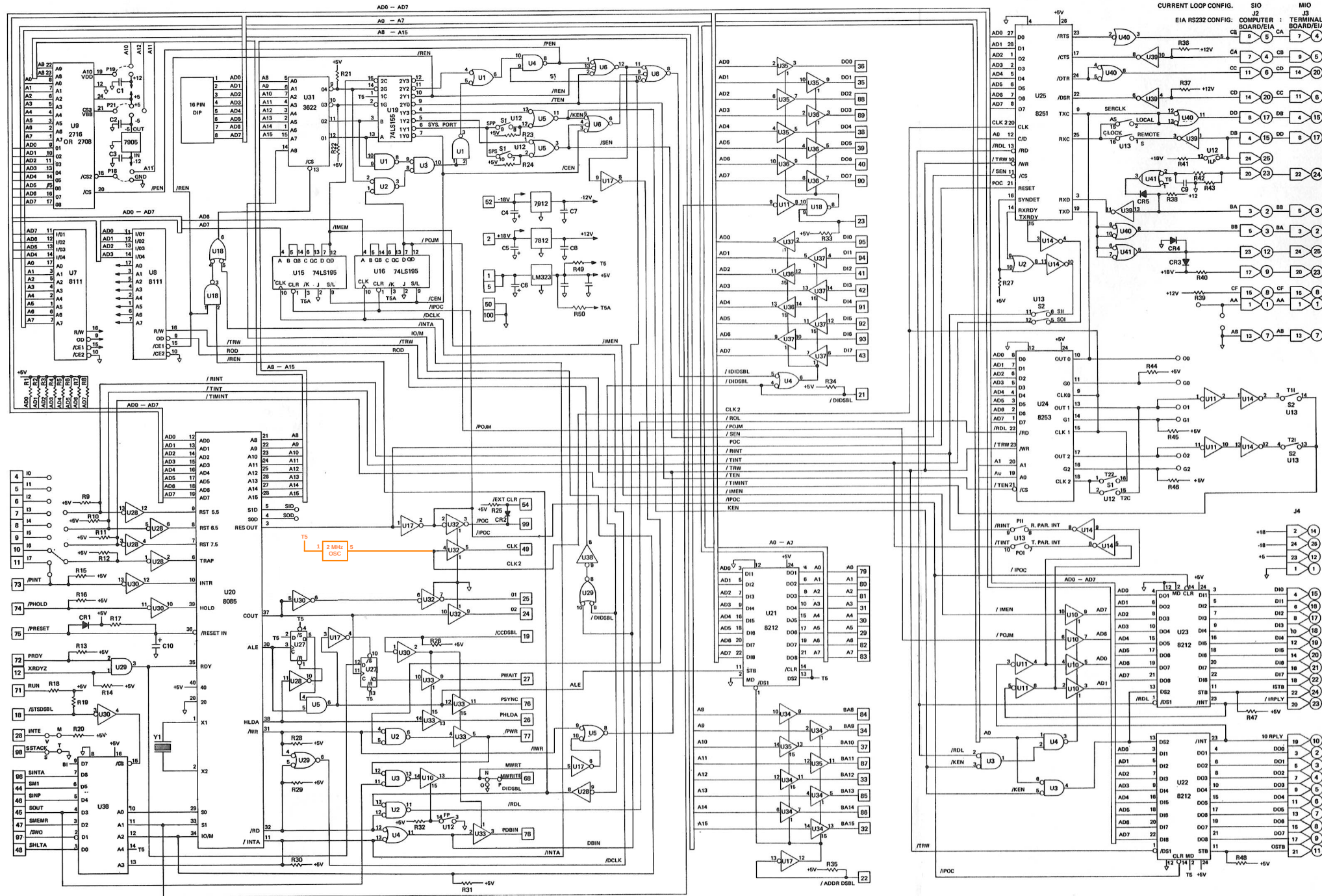




REVISIONS				
LTR	DESCRIPTION	DATE	APPROVED	
U1 U4 U18	74LS00	U39 75189 U40 75188 U41 75452	.1 uF	
U2	74LS32	C1, C2, C7, C8, C11, C12, C14, C15, C16, C18, C19, C20, C21, C22, C23, C24, C27, C28, C29, C30, C31 C32, C33, C34		
U3	74LS02	C3, C6, C9 2.2 uF		
U5 U29	74LS08	C10 4.7 uF		
U6	74LS11	CR1 IN4148		
U7 U8	8111A-4	CR2 CR5 CR3 CR4	IN270 IN4002	
U9	FIRMWARE PROM SOCKET	Y1 6MHZ		
U10 U32-U36	8T97	R9 - R16 R18 - R20 R25, R26 R33 - R35 R50	1K ¼w	
U11 U17 U28 U30	74LS04	R17, R23, R24 R28 - R32, R38		
U12	S1	R1 - R8, R21, R22 R36, R37, R39 R44 - R48	4.7 ¼w	
U13	S2	R40 750 ¼w R41 620 ¼w R42 R43		
U14	74LS05		10K ¼w	
U15	74LS195			
U16	74LS155		150 ¼w	
U19	8085			
U20	8212		150 ¼w	
U21-U23	8253-2			
U24	8251A		150 ¼w	
U25	8251A			
U27	74LS74		150 ¼w	
U31	3622			
U37	74LS367		150 ¼w	
U38	74S288			

TOLERANCES UNLESS OTHERWISE SPECIFIED		FRACTIONS DEC. ANGLES	
±	±	±	±
APPROVALS	DATE	MPU-B REV. 1.2 CLOCK GENERATOR MODS	
DRAWN		SCHEMATIC	
CHECKED		SCALE	SIZE DRAWING NO.
			B
DO NOT SCALE DRAWING		SHEET	



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