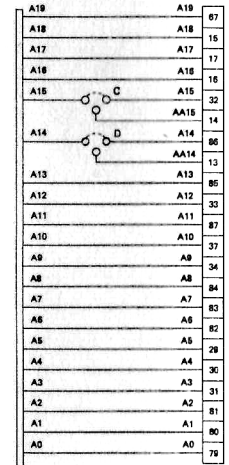
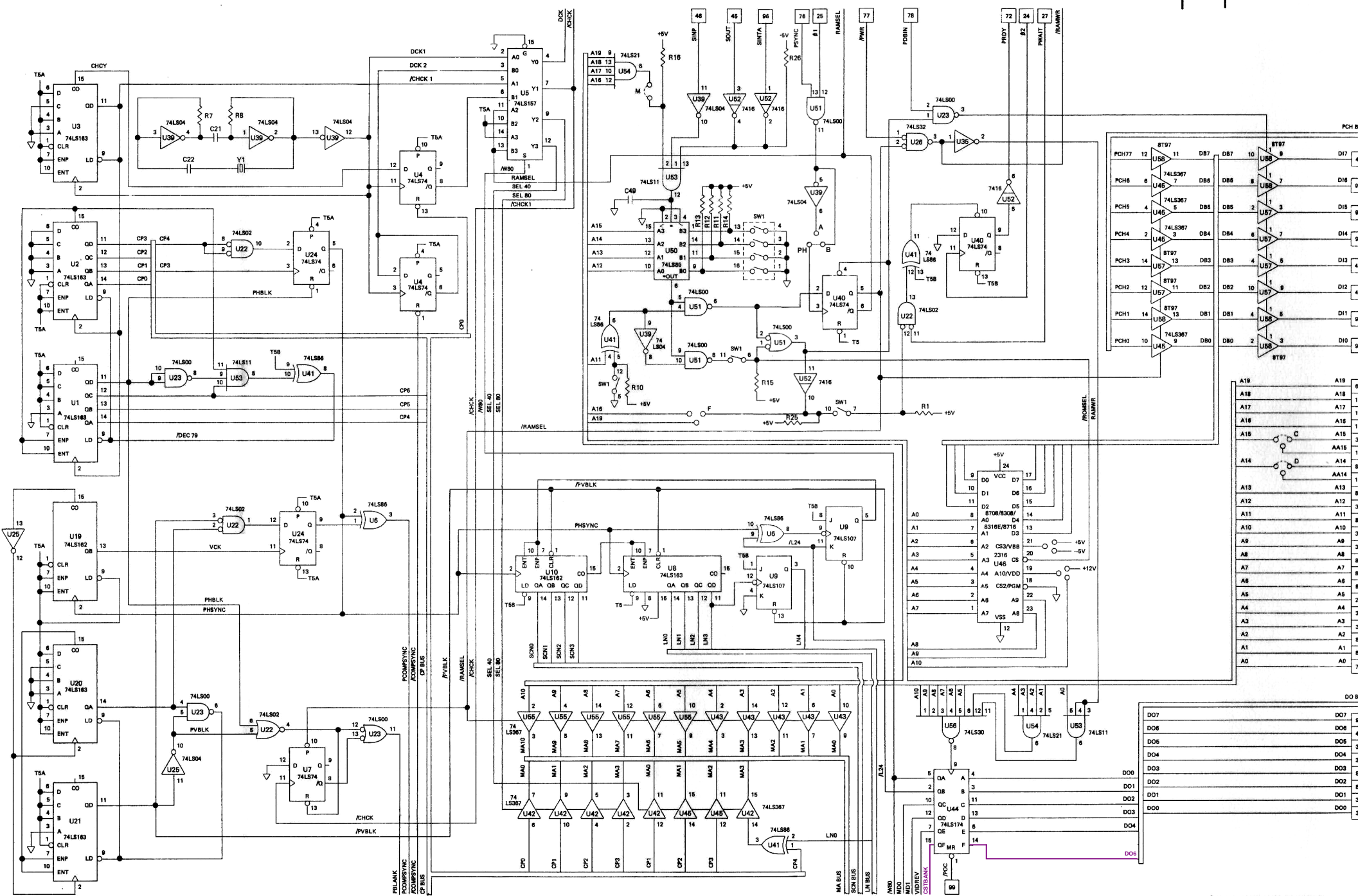


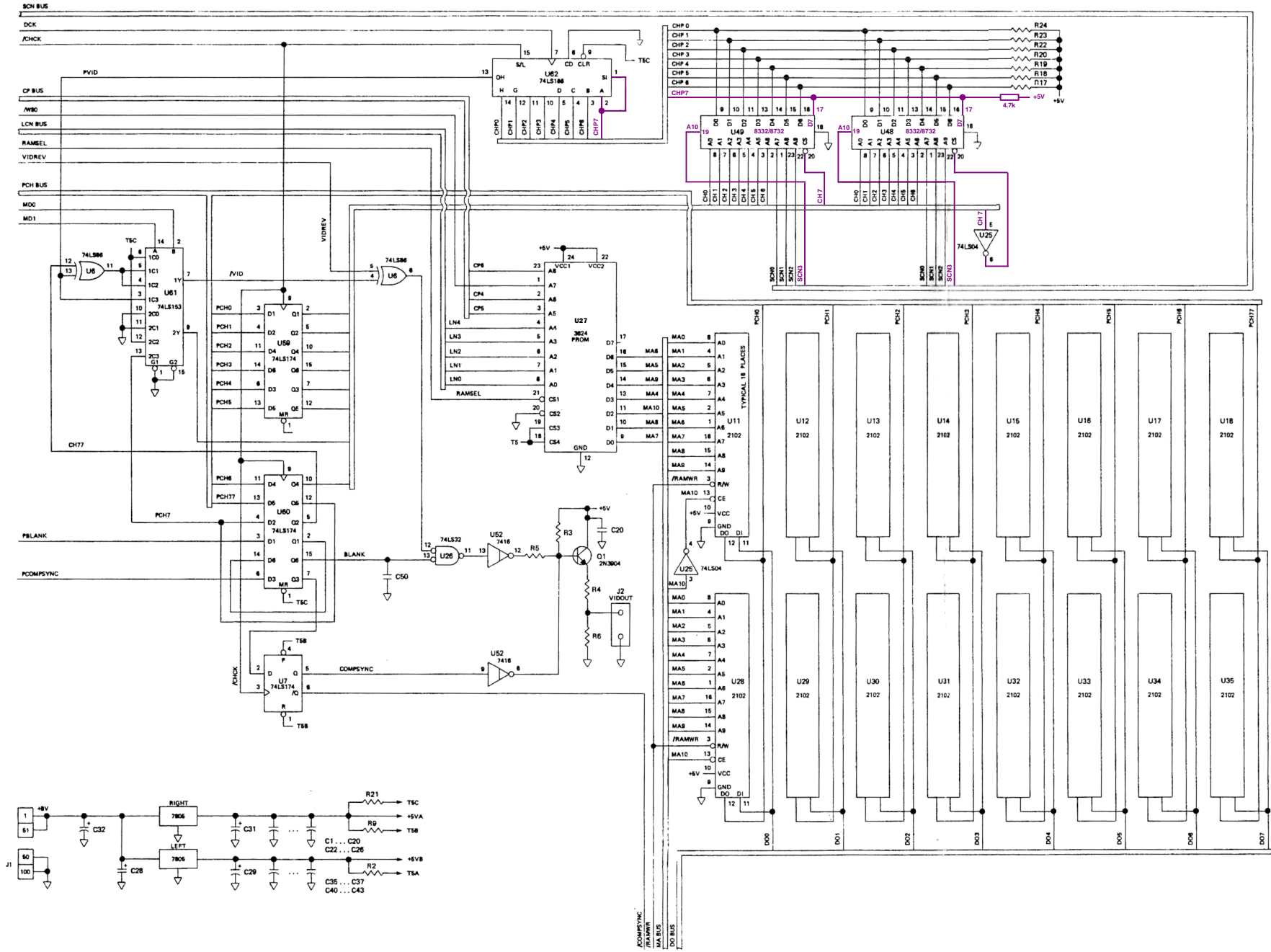
REVISIONS

LTR	DESCRIPTION	DATE	APPROVED



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APPROVALS	DATE	IMSAI SYSTEM VIO REV. 1 2732 CHARGEN MODS SCHEMATIC DIAGRAM	
DRAWN		SCALE	SIZE DRAWING NO.
CHECKED		B	
DO NOT SCALE DRAWING		SHEET 1 OF 2	

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
0	ORIGINAL REV. 1	6/77	

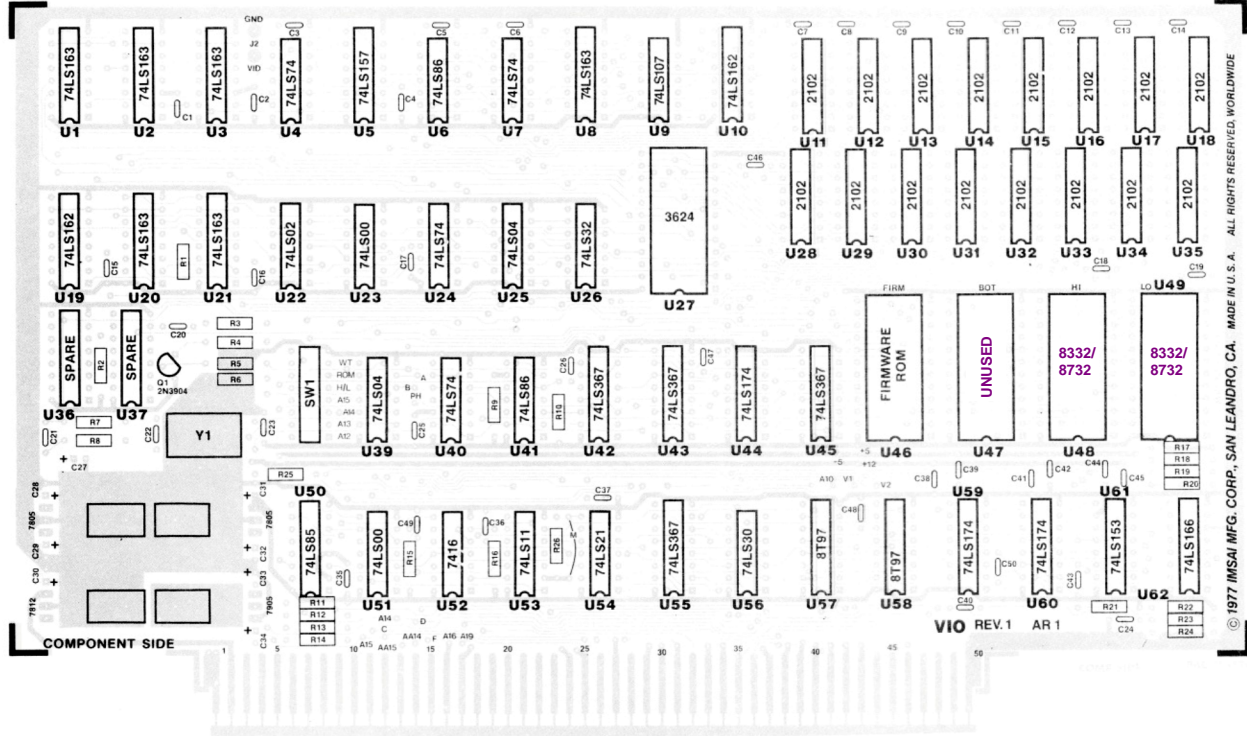


TO U48-21 AND U49-21 (A11). THESE PINS ARE CONNECTED TO THE -5V SUPPLY RAIL (NOT SHOWN ON THE SCHEMATIC). ALL COMPONENTS HAVE NOW BEEN REMOVED FROM THE -5V SUPPLY RAIL. THIS BECOMES THE CHARACTER SET BANK SWITCH.

- U1 THRU U3 74LS163
- U8 THRU U20 74LS163
- U21 THRU U4 74LS74
- U7 THRU U24 74LS74
- U40 THRU U5 74LS157
- U6 THRU U41 74LS86
- U9 THRU U10 74LS107
- U19 THRU U11 74LS162
- U18 THRU U28 2102 AL-4
- U35 THRU U22 74LS02
- U23 THRU U51 74LS00
- U25 THRU U39 74LS04
- U26 THRU U27 74LS32 3624 W/ VIO MAP
- U36 THRU U42 SPARE
- U43 THRU U45 74LS367
- U44 THRU U59 74LS174
- U60 THRU U46 2316 SMP/80.0
- U49 THRU U50 8332/8732
- U52 THRU U53 74LS85
- U54 THRU U56 74LS11
- U57 THRU U58 74LS21
- U62 THRU U61 74LS166
- C1 THRU C20 .1 uF
- C23 THRU C26 .1 uF
- C35 THRU C48 .1 uF
- C21 THRU C49 .001 uF
- C22 THRU C27 20 pF
- C34 THRU C50 2.2 uF 25V
- Q1 2N3904
- R1 THRU R10 2.2K 1/4W
- R16 THRU R25 1K 1/4W
- R26 THRU R9 470 1/4W
- R7 THRU R8 100 1/4W
- R4 THRU R6 150 1/4W
- R17 THRU R20 4.7K 1/4W
- R22 THRU R24 8 POSITION DIP
- Y1 12.2304 MHz XTAL

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FRACTIONS DEC. ANGLES			
± ± ±			
APPROVALS	DATE	IMSAI SYSTEM VIO REV. 1 2732 CHARGEN MODS SCHEMATIC DIAGRAM	
DRAWN		SCALE	SIZE DRAWING NO.
CHECKED			<b>B</b>
DO NOT SCALE DRAWING		SHEET 2 OF 2	

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
0	ORIGINAL REV. 1	6/77	



U1 THRU U3	74LS163	U61	74LS153
U8 THRU U21	74LS163	U62	74LS166
U4	74LS74	C1 THRU C20	
U7	74LS74	C23	.1 uF
U24	74LS157	THRU C35	
U40	74LS157	THRU C48	
U5	74LS86		
U6	74LS107	C21	.001 uF
U41	74LS107	C49	20 pF
U9	74LS162	C22	2.2 uF 25V
U10	2102 AL-4	C27	470 pF
U19	2102 AL-4	THRU C34	2N3904
U28	2102 AL-4	C50	2N3904
U29	2102 AL-4	Q1	
U30	2102 AL-4	R1	
U31	2102 AL-4	R10	
U32	2102 AL-4	THRU R16	2.2K 1/4W
U33	2102 AL-4	R25	
U34	2102 AL-4	R26	
U35	2102 AL-4	R2	
U49	2102 AL-4	R9	1K 1/4W
U22	74LS02	R21	
U23	74LS00	R3	
U51	74LS04	R7	470 1/4W
U25	74LS04	R8	
U39	74LS32	R4	100 1/4W
U26	3624 W/ VIO MAP	R5	150 1/4W
U27	SPARE	R6	
U36	SPARE	R17	
U37	SPARE	THRU R20	4.7K 1/4W
U46	FIRMWARE ROM	R22	
U47	UNUSED	R23	
U48	8332/8732	R24	
U49	8332/8732	THRU R24	
U59	2316 SMP/80.0	S1	8 POSITION DIP
U60	8332/8732	Y1	12.2304 MHz XTAL
U61	74LS85		
U62	7416		
	74LS11		
	74LS21		
	74LS30		
	8T97		

TOLERANCES UNLESS OTHERWISE SPECIFIED	
FRACTIONS	DEC.
±	±
±	±
±	±

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APPROVALS	DATE

IMSAI SYSTEM VIO REV. 1 2732 CHARGEN MODS ASSEMBLY DIAGRAM		
SCALE	SIZE	DRAWING NO.
	B	
DO NOT SCALE DRAWING		SHEET 2 OF 2