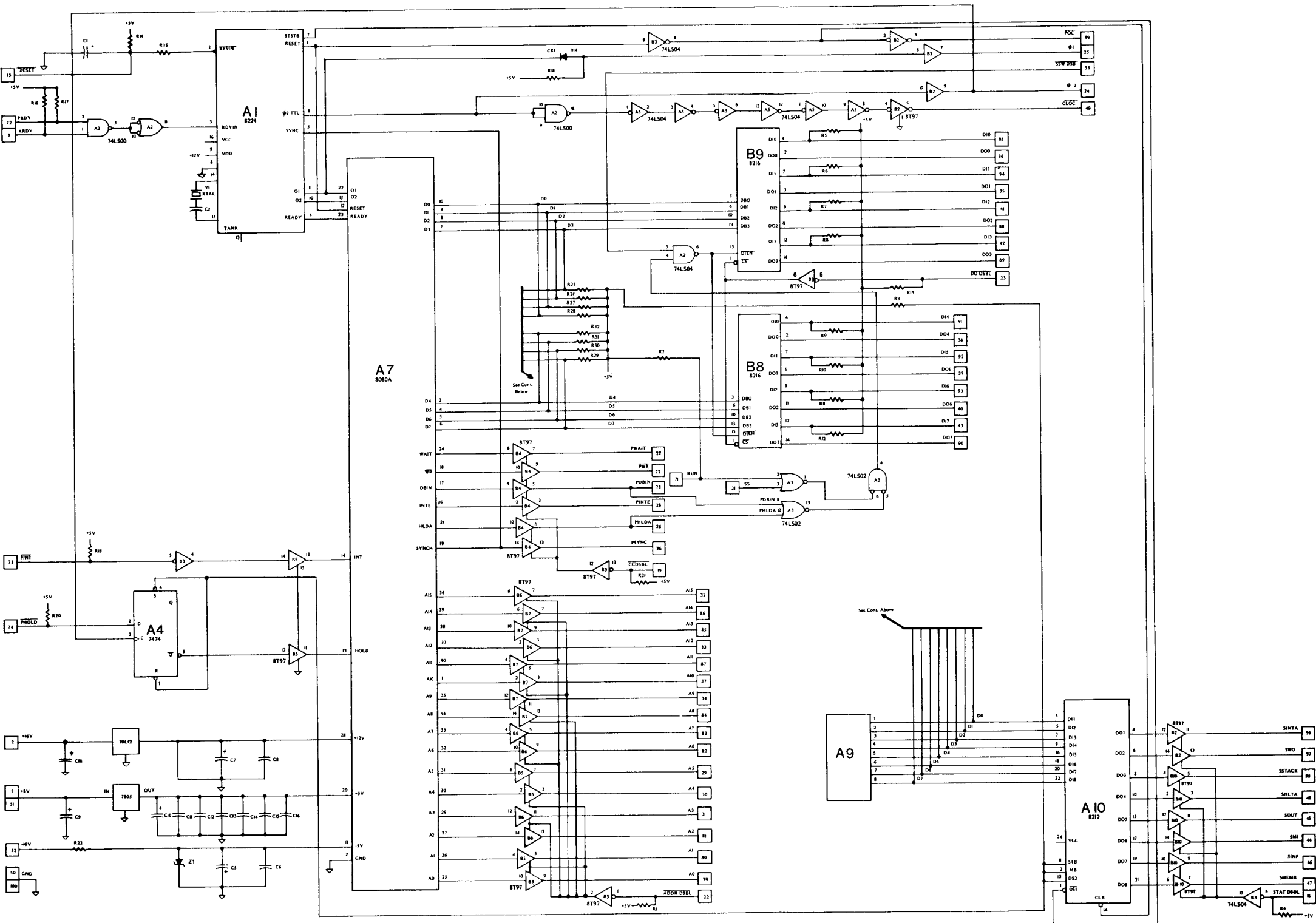


REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
0	ORIGINAL REV.2	11/75	
1	REV.4	2/76	
2	UPDATE & MOD	2/77	
3	UPDATE & MOD	1/25	



A1 8224
A2 74LS00
A3 74LS02
A4 7474
A5 74LS04
A7 8080A
A9 DATA BUS SOCKET
A10 8212

B2 8T97
B3 74LS04
B4 8T97
B5 8T97
B6 8T97
B7 8T97
B8 8216
B9 8T97
B10 8T97

C1 33uF
C5 33uF
C7 33uF
C9 33uF
C10 33uF

C6 .1uF
C8 .1uF
C11 .1uF
C16 .1uF
C18 .1uF
C2 39pF

CR1 IN914
Z1 IN751

R1 thru R13 1K 1/4w
R15 thru R17 1K 1/4w
R19 thru R21 1K 1/4w
R14 4.7K 1/4w
R18 4.7K 1/4w
R25 thru R32 470 1/4w
Y1 18uHz

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TOLERANCES UNLESS OTHERWISE SPECIFIED			
FRACTIONS	DEC.	ANGLES	
±	±	±	
APPROVALS		DATE	
DRAWN			
CHECKED			
SCALE	SIZE	DRAWING NO.	
DO NOT SCALE DRAWING			SHEET

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SCHEMATIC DIAGRAM
MPU-A REV.4 2/76